

ISPEC 2025

AGENDA

DATE	TIME	SESSION NAME	BROAD AREA	SESSION THEME	COMPANY / SPEAKER
Day 0 – Mar 4 - PDC Courses For Industry Engineers at IIT Gandhinagar on Mar 4, 2025					
IIT Gandhinagar, Conference room 1 - Design and devices					
4th March 2025	9:00AM - 10:15AM	Professional Development Courses for Grad Students and Industry Engineers	Design & Devices	System Design & Architecture	Prof. Binod Kumar (IITJ)
	10:15AM - 11:30AM			Package Design	Prof. Rohit Sharma (IITRPR)
	11:30AM - 12: 45PM			Devices – CMOS	Prof. Nihar Mohapatra (IITGN)
	12:45PM - 2:00PM	Lunch Break			
	2:00PM - 3:15PM	Professional Development Courses for Grad Students and Industry Engineers	Design & Devices	Devices – Power	Prof. Akshay K (IITBBS)
	3:15PM - 4:30PM			Integrated Sensors & MEMS	Prof. Bhaskar Mitra (IITD)
IIT Gandhinagar, Conference room 2 - Package Substrates and Assembly					
	9:00AM - 10:15AM	Professional Development Courses for Grad Students and Industry Engineers	Package Substrates and Assembly	Interconnecting Substrates	Prof. Pradeep Dixit (IITB)
	10:15AM - 11:30AM			Co-Packaged Optics	Prof. Naresh Emani (IITH)
	11:30AM - 12 :45PM			Materials – Device, Components & Packaging	Prof. Bhagwati Prasad (IISc) Prof. Murali KP (NITC) Prof. Praveen Kumar (IISc)
	12:45PM - 2:00PM	Lunch Break			
	2:00PM - 3:15PM	Professional Development Courses for Grad Students and Industry Engineers	Package Substrates and Assembly	Predictive Modelling & Design	Prof. Tarun Agarwal (IITGN)
	3:00PM - 4:30PM			IC & Board Assembly and Reliability	Prof. Nilesh Badwe (IITK)
IIT Gandhinagar, Conference room 3 - Integrated Packages & Systems					
	9:00AM - 10:15AM	Professional Development Courses for Grad Students and Industry Engineers	Integrated Packages & Systems	Integrated Power Electronics	Prof. Shiladri Chakraborty (IITB)
	10:15AM - 11:30AM			Thermal Design & Technologies	Prof. Anandaroop B (IITKGP)
	11:30AM - 12:45PM			6G Integrated Systems	Prof. Mrinal Mandal (IITKGP)
	12:45PM - 2:00PM	Lunch Break			
	2:00PM - 3:15PM	Professional Development Courses for Grad Students and Industry Engineers		System Level Electrical Test	Prof. Jaynarayan Tudu (IITTP)
Day 1 - H1 (Main Hall) - INAUGURAL SESSION - ALL TOGETHER- Mahatma Mandir, Gandhinagar					
5th March 2025	9:00AM - 1:00PM	Inaugural Session			
	9.00AM - 10.30AM	Inaugural	Welcome address and Visionary Keynotes	Powering India's Semiconductor Revolution: Charting the Future at Vision Summit and ISPEC2	
			Visionary Keynotes 5		
	10.30AM - 11:00AM	Inaugural	Coffee Break Networking		
	11:00AM - 12:30PM	Inaugural	Grand Inaugural session by VVIPs/ Dignitaries, Semiconductor Manufacturing Supply Chain Report launch	Viksit Bharat: The Crucial Role of Semiconductors in India's Growth, Development and 5 Trillion economy	

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DATE	TIME	SESSION NAME	BROAD AREA	SESSION THEME	COMPANY / SPEAKER	
	12:45PM		Exhibition Inauguration	Sand to Silicon to Systems : Experience the End-to-End Ecosystem Journey		
	12:45PM - 2:00PM	Lunch Break				
Day 1 - H2-Mar 5 - ISPEC Keynote Sessions						
5th March 2025	2:00PM- 6:00PM	ISPEC Keynote Session	Welcome address and Platinum Keynote Talks	Welcome	Prof. Rajat Moona, Director, IITGN	
				IDSPS Program: Vision, Status & Plans	Prof. Rao Tummala	
				Semiconductor Focus at DST	Prof. Abhay Karandikar	
				Status & Plans of ISM	CEO of ISM (Tentative)	
				Kaynes Advanced Packaging Roadmap	Dr. Mahadevan Iyer	
				Keynote 1	TBA	
	Coffee Break					
			ISPEC Keynote Session	Welcome address and Platinum Keynote Talks	Keynote 2	TBA
					Keynote 3	TBA
					Keynote 4	TBA
		Keynote 5			TBA	
				Keynote 6	TBA	
Day 2- H2- Mar 6 - ISPEC R&D Session: Next Gen Research in 12 Strategic Technologies Session Chairs: Ravi M, Intel and Prof. Abhisek D, IITD						
6th March 2025	9:00AM - 9:30AM	ISPEC R&D Session	Next Gen Research in 12 Strategic Technologies	System Design & Architecture	Prof. Binod Kumar (IITJ)	
	9:30AM - 10:00AM			Devices - CMOS	Prof. Nihar Mohapatra (IITGN)	
	10:00AM - 10:30AM			Devices - Power	Prof. Akshay K (IITBBS)	
	10:30AM - 11:00AM			Interconnecting Substrates	Prof. Pradeep Dixit (IITB)	
	Coffee Break					
		ISPEC R&D Session	Next Gen Research in 12 Strategic Technologies	Co-Packaged Optics	Prof. Naresh Emani (IITH)	
	11:00AM - 11:30AM			Predictive Modelling & Design	Prof. Tarun Agarwal (IITGN)	
	11:30AM - 12:00PM			6G Integrated Systems	Prof. Mrinal Kanti Mandal (IITKGP)	
12:30PM - 1:00PM	Integrated Sensors & MEMS			Prof. Bhaskar Mitra (IITD)		
1:00PM - 1:30PM	Lunch Break					
	ISPEC R&D Session	Next Gen Research in 12 Strategic Technologies	Materials - Device & Packaging	Prof. Bhagwati Prasad (IISc)		
2:30PM - 3:00PM			IC & Board Assembly and Reliability	Prof. Nilesh Badwe (IITK)		
3:00PM - 3:30PM			Power Electronics	Prof. Shiladri Chakraborty (IITB)		
3:30PM - 4:00PM			Thermal Management	Prof. Anandaroop Bhattacharya (IITKGP)		
4:00PM - 4:30PM			System Level Electrical Test	Prof. Jaynarayan Tudu (IITTP)		
4:30PM - 5:00PM						

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DATE	TIME	SESSION NAME	BROAD AREA	SESSION THEME	COMPANY / SPEAKER
Day 2 – Mar 6 - ISPEC Student Poster Session: Next Gen Research Projects in 12 SRAs Session Chairs: Dr. Arun C, Intel and Prof. Nilesh B, IITK					
6th March 2025	5:30PM- 8:00PM	ISPEC Student Poster Session	Next Gen Research Projects in 12 SRAs	System Design & Architecture	9
				Devices – CMOS	8
				Devices – Power	10
				Interconnecting Substrates	9
				Co-Packaged Optics	4
				Predictive Modelling & Design	8
				6G Integrated Systems	8
				Coffee Break	
		ISPEC Student Poster Session	Next Gen Research Projects in 12 SRAs	Integrated Sensors & MEMS	10
				Materials – Device & Packaging	16
				IC & Board Assembly and Reliability	10
				Power Electronics	8
				Thermal Management	11
				System Level Electrical Test	8
Dinner					
Day 3 – Mar 7 - ISPEC Workforce Session Session Chairs: Ravi Bhatkal, MacDermid & Prof. Shiv Govind, IITH					
7th March 2025		ISPEC Workforce Session		Manufacturing Skill Development	Dr. Ashwini Agarwal
				Current Technology Workforce	Sharat Kaul, Kaynes
				Next-gen Technology Workforce	Prof. Nilesh Badwe, IITK
Day 3 – Mar 7 - ISPEC Supply Chain Session Session Chairs: Venky S, 3D Systems & Kuldeep J, AtoTech					
7th March 2025		ISPEC Supply Chain Session			Venky S and Kuldeep J
Day 4 – Mar 8 - Site Visit to Dholera & Sanand					
8th March 2025	9:00AM - 5:00PM	Site Visit to Dholera and Sanand	Visit to industrial Site 9 AM - 5 PM. On pre approval basis. Two separate routes.	Gujarat at the Heart of the Semiconductor Manufacturing Revolution: Exploring Dholera and Sanand Sites	Dholera/ Sanand visit

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EXHIBITION				
5th Mar 2025	12:45PM	Exhibition	Inauguration of Exhibition by 12.45 PM	Sand to Silicon to Systems : Experience the End-to-End Ecosystem Journey
	1:00PM - 6:00PM		Exhibition Open for delegates	
6th Mar 2025	9:15AM - 1:00PM		Exhibition Open for delegates	
	1:00PM - 6:00PM		Exhibition Open for delegates	
7th Mar 2025	9:15AM - 1:00PM		Exhibition Open for delegates	
	1:00PM - 6:00PM		Exhibition Open for Students and Public	

Date	TIME	Session Name	Broad Area	Session Theme
PARALLEL SESSIONS				
5th Mar 2025			India Taiwan Roundtable	45-50 CXO Roundtable
			Roundtable with Infineon	30-35 Stakeholders roundtable
			Meetings in Meeting Rooms 1-9	Key Sponsor/Stakeholder/Govt Meetings
6th Mar 2025			India Japan Roundtable	30-40 Stakeholders Roundtable
			Meetings in Meeting Rooms 1-9	Key Sponsor/Stakeholder/Govt Meetings
			Round table with Embassies	Embassies
			India Singapore - round table	20-25 Stakeholders
5th/6th/7th Jan 25			Anchor Investors - meeting rooms	
			GoG - meeting room	
			Gol - meeting room	
			Korea - Round table - To be decided	
			US Country Round table - To be decided	